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January 13, 2004

David V. Carlson Direct Line: (206) 694–4828 carlsond@seedlaw.com

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Facsimile Transmission 5 pages including this page				
TO: Filing Receipt Corrections  Fax No.: 1 703 746 9195  Phone No.: 1 703 308 1202				
RE: U.S. Patent Application No. 10/612,825  PROCESS FOR RUNNING PROGRAMS WITH SELECTABLE INSTRUCTION LENGTH PROCESSORS AND CORRESPONDING PROCESSOR SYSTEM  Our Reference: 851763.434				
☐ Urgent 🗵 For Review ☐ Please Confirm Receipt ☐ Please Reply ASAP				
Comments:				
If you do not receive all pages, please call <u>Jason Añover</u> at (206) 622-4900 or fax our office.				
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**PATENT** 

I hereby certify that this paper is being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.

Jahmry 13, 200

Date

Jason Añover

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Applicants** 

Fabrizio Simone Rovati et al.

Application No.

10/612,825

Filed

July 1, 2003

For

PROCESS FOR RUNNING PROGRAMS WITH SELECTABLE

INSTRUCTION

LENGTH

PROCESSORS

AND

CORRESPONDING PROCESSOR SYSTEM

Art Unit

2183

Docket No.

851763.434

Date

January 13, 2004

Fax to 703-746-9195
Filing Receipt Corrections
Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

## REQUEST FOR CORRECTED FILING RECEIPT

### Commissioner for Patents:

Attached is a copy of the official Filing Receipt received from the PTO in the above-identified application, for which issuance of a corrected Filing Receipt is respectfully requested.

There is an error with respect to the following data, which is incorrectly entered.

There is an error in the title, which should read:

PROCESS FOR RUNNING PROGRAMS WITH SELECTABLE INSTRUCTION LENGTH PROCESSORS AND CORRESPONDING PROCESSOR SYSTEM

The correction to be made has been marked on the attached copy of the Filing Receipt.

> Respectfully submitted, Seed Intellectual Property Law Group PLLC

David V. Carlson Registration No. 31,153

### Attachment:

Copy of Filing Receipt w/corrections

701 Fifth Avenue, Suite 6300 Seattle, Washington 98104-7092 Phone: (206) 622-4900 Fax: (206) 682-6031

445370

Page 1 of 2

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APPL NO. 10/612,825

FILING OR 371 (c) DATE 07/01/2003 ART UNIT 2183

FIL FEE REC'D 900

ATTY.DOCKET NO 851763,434

DRAWINGS TOT CLMS

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00500 SEED INTELLECTUAL PROPERTY LAW GROUP PLLC 701 FIFTH AVE **SUITE 6300** SEATTLE, WA 98104-7092

**CONFIRMATION NO. 3917 UPDATED FILING RECEIPT** OC000000011506320\*

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DEC 1 9 2003

Date Mailed: 12/16/2003

SEED INTELLECTUAL PROPERTY LAW GROUP PLLC

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if

## Applicant(s)

Fabrizio Simone Rovati, Cinisello Balsamo, ITALY; Antonio Maria Borneo, Matera, ITALY; Danilo Pietro Pau, Sesto San Giovanni, ITALY:

# **Assignment For Published Patent Application**

STMicroelectronics S.r.I., Agrate Brianza, ITALY;

Domestic Priority data as claimed by applicant

## Foreign Applications

EUROPEAN PATENT OFFICE (EPO) 02425436.9 07/02/2002

If Required, Foreign Filing License Granted: 09/26/2003

Projected Publication Date: 03/25/2004

Non-Publication Request: No

Early Publication Request: No

Title

Page 2 of 2

with selectable instruction tength

Process for running programs on processors and corresponding processor system

Pr liminary Class

712

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